



Rajendran Nair, 3838 E. Encinas Ave, Gilbert, AZ 85234

Re: 10/714,424

EFW

THE UNITED STATES PATENT AND TRADEMARK OFFICE
PATENT APPLICATION DOCUMENTATION

COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313 – 1450
Attn: Edward H. Tso, Facsimile # 571-273-8300

January 19, 2007

Please find enclosed a response to an office action received for:

Patent application serial no.: 10/714,424

In response to OA mailed: 11/29/2006

Please acknowledge the receipt of this letter and response to the office action. Kindly address all correspondence to the below named inventor.

Sincerely,

Rajendran Nair
3838 E. Encinas Ave.
Gilbert, AZ 85234

Sent by US Mail

PRIORITY MAIL	Label Number: _____	Date: <u>January 19, 2007</u>
I hereby certify that this paper and enclosures and/or fee are being deposited with the United States Postal Service 'Priority Mail Post Office to Addressee' service under 37 CFR §1.10 on the date indicated above and is addressed to: Commissioner for Patents , P.O. Box 1450, Alexandria, VA 22313-1450		
 Senders name	 Signature	

Application #: 10/714,425	F-date: 11/17/2003	Exmnr/Art Unit: Edward H. Tso/2838
Response mailing date: 01/19/2007		Letter reference: OA_10/714424_R3

Response to Office Action dated 11/29/2006

1. Examiner Edward H. Tso's office action summary and explanation mailed on 11/29/2006 is hereby acknowledged.
2. The specification has been reviewed to correct for grammatical, formatting, spelling and other minor errors. No new material has been added.
3. Claims 1, 4-13 and 18 have been edited to include a) identifiers such as (CURRENTLY AMENDED), (WITHDRAWN) etc., as well as to indicate text that has been eliminated as in ~~eliminated text~~.
4. No new material has been added to claims 1, 4-13 and 18, while they have been amended and corrected to provide clear structural recitation of interconnecting circuits. Claim 18 has been further corrected to be a limiting claim dependent upon claim 1.
5. The applicant respectfully disagrees with the examiner's assertion that claims 1, 4-13 and 18 of the application are anticipated by Thomas et al. (US 6,331,763) for the following reasons:
 - a. US 6,331,763 (Thomas-763) claims, in its principal claim 1, a regulation circuit in parallel with the chargeable device (a "shunt regulator") comprising a thermally sensitive resistor ("thermistor") in series with the current flow path that limits the current flow into the charging device in the event of an over-voltage or over-current condition, which bears no direct resemblance to the primary claim